CLAIMS ALLOWED

Total Claims

Print Claim for O.G.

Jc872 U.S. PTO 09/986008	11/07/01	•	Class Subclass	ISSUE CLASSIFICATION		<u> </u>	F			PATENT NUMBER
U.S. UTILITY Patent Application										
O.I.P.E. PATENT DATE SCANNED QUARTE										:
· [_	DDI 10.17									,-
L		PPLICATION NO. 09/986008			NT/PRIOR	CLASS 029	SUBCLASS 847	ART UNIT 3729	EXAMINER	rc
APPLICANTS	Tom	ahir o Ro: ohir hino:	kug n N	leuwa Jenni	EL Aleman		1 - 11		Cho	1
TITLE	Process for producing multilayer circuit board									
							•	-		12/99
1						ICCLUN	0 01 400IE			

ISSUING CLASSIFICATION										
0	RIGINAL		CROSS REFERENCE(S)							
CLASS	SUBCLA	SS CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNAT	IONAL CLASSIFICA	TION								
			Continued on Issue Slip Inside File Jacket							

DRAWINGS

Figs. Drwg.

Print Fig.

Sheets Drwg.

TERMINAL DISCLAIMER

☐ The term of this patent			NOTICE OF ALLOWANCE MAILED		
subsequent to (date) has been disclaimed.	(Assistant Examiner)	(Date)			
The term of this patent shall not extend beyond the expiration date					
of U.S Patent. No.			ISSUE FEE		
			Amount Due	Date Paid	
	(Primary Examiner)	(Date)			
☐ The terminalmonths of			ISSUE BATCH NUMBER		
this patent have been disclaimed.	(Legal Instruments Examiner)	(Date)	N		
WARNING: The information disclosed herein may be restrict Possession outside the U.S. Patent & Trademark	ed. Unauthorized disclosure may be Office is restricted to authorized empl	prohibited by the L	United States Code Title 35, tors only.	Sections 122, 181 and 388.	
Form PTO-438A Rev. 6/99)		FILED WITH:	DISK (CRF)	FICHE CD-ROM ached in pocket on right inside flap)	